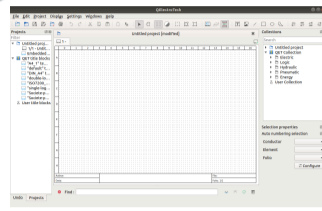


QElectroTech-documentatie



Basisprincipes



Koppel



Voorkeuren



Project



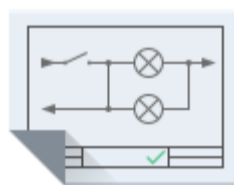
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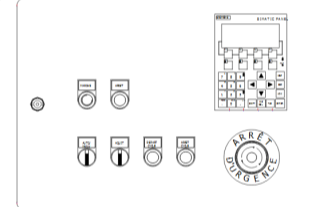
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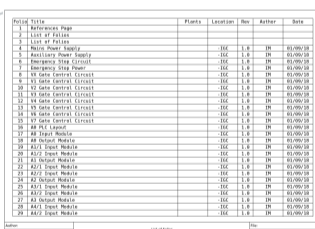
Geleider



Schema



Tekening



Rapporten



Exporteren en afdrukken



Bijlage

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